

▲ **Figure 1b.** A low value resistor placed on the common cathode node of the varactors, where there is the symmetry plane of the VCO.

line. The voltages in phase opposition are summed at the output by the properly loaded and dimensioned coupler.

The coupler is very important for this topology of VCO. It has been designed by placing two sources in phase opposition where the negative resistances will operate. The termination load for the best summation of voltages is not a 50 ohm resistor but a capacitor with a value that varies with the gap and the length of the coupler. The impedance of the sources is an estimate of the average value presented by the active devices. Although this procedure is not rigorous, it is useful at least for a coarse design of the coupler. Tuning of capacitance, gap and length of the coupler will allow optimization during non-linear simulation of phase noise.

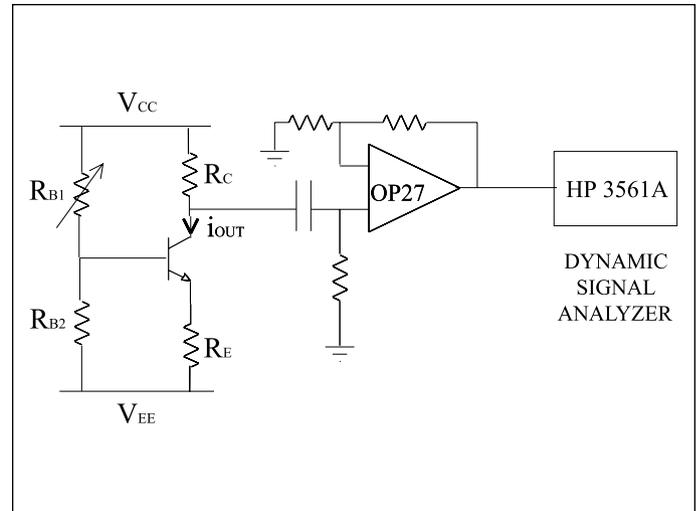
Two coupled oscillators can oscillate in the phase or antiphase mode and, in order to ensure the latter, the former can be suppressed by placing a low value resistor on the common cathode node of the varactors, where we have the symmetry plane of the VCO (Figure 1b).

Because of symmetry and phase opposition, the middle of the resonator behaves as a voltage node, and here the varactors experience the lowest RF voltage swing.

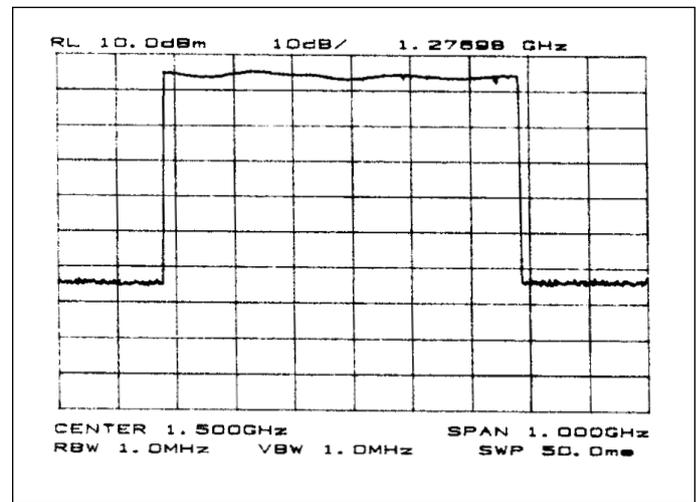
Models

The behavior of the circuit has been simulated with the software tool HP MDS. The BJT is the Avantek AT 42035. For the BJT, the nonlinear model based on the data reported in the datasheets has been used. This nonlinear model has been completed (for the noise analysis) with the A_f and k_f parameters for the BJT's flicker noise:

$$S_B(\omega) = \frac{d \langle i_B^2 \rangle}{df} = 2qI_B + k_f \frac{I_B^{A_f}}{f},$$



▲ **Figure 2.** The test bench for the measurement of the corner frequency of the BJT, operated at rest.



▲ **Figure 3.** The tuning band results of the measurements.

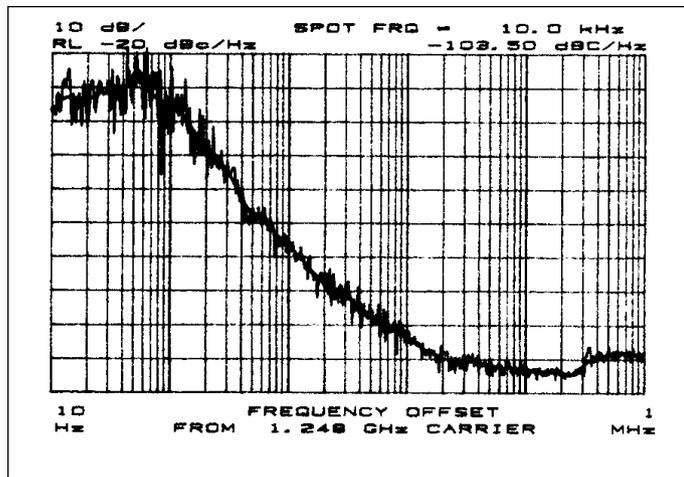
where I_B is the base current. The values for these parameters have been obtained by a measurement of the corner frequency of the BJT operated at rest. The test bench for this measurement is shown in Figure 2.

For the low noise measurement test bench, the OP27 opamp has been used with metal film resistors, ceramic capacitors and battery supply voltages [4]. Two measurements at two different collector currents allowed the solution of a nonlinear algebraic system for A_f and k_f .

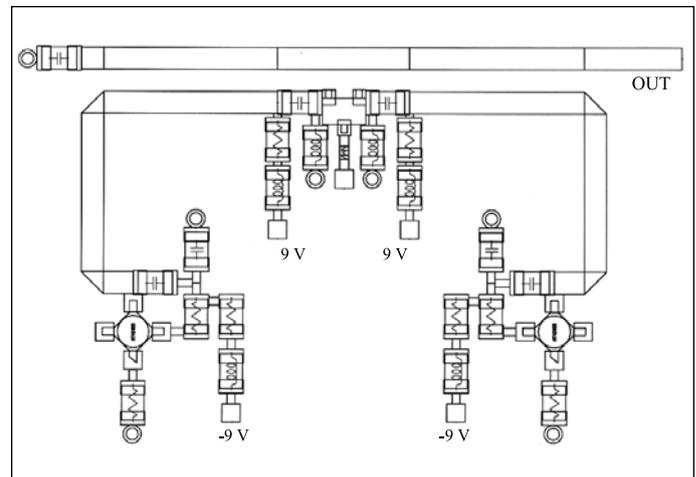
The values $A_f = 1.304$ and $k_f = 1.574 \cdot 10^{-14}$ have been introduced in the model in MDS.

The varactor used is an Alpha SMV 1206-004 with junction capacitance, $C_j(V_A)$, modeled as indicated in [5], by fitting the curve reported in the datasheets; two devices are housed in SOT23 package.

Resistors (Philips), capacitors (ATC) and inductors



▲ Figure 4. The noise-to-noise carrier ratio for VTUNE=3 V.



▲ Figure 5. The layout of the VCO.

(AVX) have been described by the equivalent models suggested in the datasheets.

The simulations show that the oscillation frequency of the VCO ranges from 1.267 to 1.815 GHz, with tuning voltage from 0 to 22 V.

Careful dimensioning of negative resistance and resonator is necessary to intersect their reflection coefficients Γ_D^{-1} and Γ_R orthogonally, minimizing phase variations due to active devices for the whole voltage tuning range of varactors [6].

The results of the measurements agree with the results of the simulation. The tuning band ranges from 1.187 to 1.780 GHz (Figure 3): it is a 40 percent band around a 1.48 GHz center frequency. The output power of the VCO is between 4.4 and 5.6 dBm over the entire tuning band.

The measurement of the phase noise $L(f_m)$ has been realized by locking the oscillation frequency of the VCO with a narrow band PLL. Thus the phase noise levels measured represent the intrinsic phase noise limits of the VCO, and are not corrupted by contributions associated with the long term frequency fluctuations of the free-running VCO. The resulting phase noise level $L(f_m)$ @ $f_m = 10$ kHz is lower than -103 dBc/Hz over the entire tuning band. The noise-to-carrier ratio $L(f_m)$ for $V_{TUNE} = 3$ V is shown in Figure 4. The layout of this VCO is shown in Figure 5.

Conclusion

A theoretical explanation for the behavior of a push-push oscillator has been proposed. The good phase noise performance of this configuration has been experimentally proved by realizing a wideband push-push VCO

(600 MHz tuning band around a 1.5 GHz center frequency) with less than -103 dBc/Hz over the entire tuning band.

More detailed simulations may be performed with low noise dynamic measurements of the devices because there are big differences in flicker noise distribution compared to static conditions. ■

References

1. Razavi, "RF Microelectronics," Prentice Hall.
2. Chang, Cao, Mishra, York, "Phase Noise in Coupled Oscillators: Theory and Experiment," *IEEE Trans. MTT*, Vol. 45, No. 5, May 1997.
3. Yabuki, Sagawa, Makimoto, "VCOs for Mobile Communications," *Applied Microwave and Wireless*, Winter 1991/92.
4. Neri, Pellegrini, Saletti, "Ultra Low-Noise Preamplifier ...," *IEEE IM*, Vol. 40, No. 1, February 1991.
5. Cojocar, Brazil, "A Large-signal Equivalent Circuit Model for Hyperabrupt p-n Junction Varactor Diodes," *Proc. of the 1992 European Microwave Conference*.
6. Kurokawa, "Some Basic Characteristics of Broadband Negative Resistance Oscillator Circuits," *Bell Sys. Tech. J.*, July-August 1969.

Author information

Marco Gris is employed by Alcatel Telecom in Concorezzo, Italy, and is also associated with the Department of Electronics at the University of Pavia, Italy. He may be contacted by phone at 0039-039-6865667 or via e-mail at marco.gris@netit.alcatel.it.